

ABSTRACT

A liquid crystal display device that is capable of preventing a deterioration of liquid crystal generated at the outer area of an IPS mode TFT array. In the device, a plurality of data lines apply data signals to a thin film transistor array and a plurality of gate lines apply gate signals to the thin film transistor array. A plurality of gate links is extended from the gate lines into the outer area of the thin film transistor array. A plurality of common voltage lines is provided in such a manner to cross the gate links to apply a common voltage to the thin film transistor array. Accordingly, a deterioration of liquid crystal caused by the gate voltage at the outer area of the TFT array can be prevented to eliminate a generation of stain.